Roll No.

Total Pages : 03

BT-4/M-20 34009 DIGITAL ELECTRONICS EE-204-E

Time : Three Hours]

[Maximum Marks: 100

Note Attempt*Five* questions in all, selecting atmeeast question from each Unit.

Unit I

- **1.** (a) If (211) = (152) find the value of x.
 - (b) Subtract79.625from 27.125using 12 bit 2's complement method.
 - (c) Convert (4057 060 Hexadecimal.
 - (d) Add 1010.11 + 1101.10 + 1001.11 + 1111.11.
 - (e) What should be the value of Y $so_1 Xhat X$ to complement $_2of_1A_0$. Explain the reason for your answer. $5 \times 2 = 10$



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2. (a) Show that :

5

 $A\overline{B}C + B + \overline{B}D + \overline{A}B\overline{D} + AC = B + C$

(b) Draw the simplestpossiblelogic diagramthat
 implements the output of the logic diagram shown
 below : 5



(b) Implement the following function using 8 : 1 MUX : F = 32 (0, 1, 3, 6, 8, 9, 10, 12, 13, 14)

Unit III

- 5. (a) Convert a S-R flip-flop to J-K flip-flop. 5
 - (b) Draw the logic and timing diagrams for SISO, shift right register for data input 1010.
- 6. (a) Design a 4-bit weighted resistor DAC whose full scale output voltage is (+5 V). The logic levels are I = +5 V and O = 0 V. Find the output voltage for input 1101.
 - (b) What is the difference between up and down counter? Design a MOD 13 counter and draw its neat diagram.

Unit IV

- 7. (a) what is interfacing of two logic families ? Explain twith the help of CMOs driving TTL logic family.
 5 (b) What is a logic family? Classify the important ones.
 5
 8. (a) State the difference between ROM, PROM, EPROM and EEPROM.
 - (b) What is PLA ? Describe its uses. 5

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